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THIN FILM TRANSISTOR (54) MANUFACTURE OF

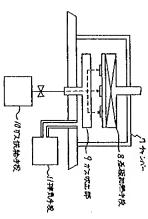
(57) Abstract:

operation characterized by high by thermal CVD of high-order silane mobility, by using a silicon film made PURPOSE: To perform stable

such as trisilane or higher as a channel semiconductor film of a thin film transistor.

thermal decomposition reaction on on the surface of the substrate by the substrate. a chamber 7; and the film 4 is formed the trisilane or higher is introduced in 400°C; the high order silane such as is heated to a temperature of about 4 is formed as follows: the substrate transistor is formed. The silicon film and a metal film, are formed. An doublelayer structure of a P-or N-type trisilane or higher is formed by a silicon oxide film and silicon nitride evaporation, sputtering and the like. substrate 1, a gate 2 comprising Ni, inverted staggered type thin film low resistance semiconductor film A source 5 and a drain 6, which have thermal CVD method on the film 3. film 4 of high-order silane such as and the like on the gate 2. A silicon film is laminated by a CVD method A gate insulating film 3 such as a W, Mo and the like is formed by CONSTITUTION: On an insulating

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